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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alfred Haeusler et al.

Art Unit: 2812

Serial No.: 10/824,745

Examiner: Anya, Igwe U

Filed: 04/15/2004

Docket No.: TID-34858

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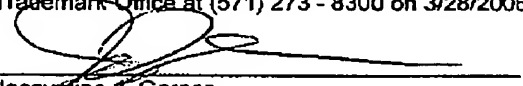
Conf. No.: 7054

For: METHOD OF FABRICATING AN INTEGRATED SILICON-GERMANIUM  
HETEROBIPOLAR TRANSISTOR AND AN INTEGRATED SILICON-GERMANIUM  
HETEROBIPOLAR TRANSISTOR

## AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being  
transmitted by facsimile to the U.S. Patent and  
Trademark Office at (571) 273 - 8300 on 3/28/2006.  
Jacqueline J. Garner  
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 10/05/2005. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

**Amendments to the Claims** are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 5.

TI-34858

1